



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/928,818	08/13/2001	Eric N. Mann	0325.00484	8343
21363	7590	07/08/2003		
CHRISTOPHER P. MAIORANA, P.C. 24025 GREATER MACK SUITE 200 ST. CLAIR SHORES, MI 48080			EXAMINER	
			COX, CASSANDRA F	
			ART UNIT	PAPER NUMBER
			2816	

DATE MAILED: 07/08/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/928,818	MANN ET AL. <i>W</i>
	Examiner	Art Unit
	Cassandra Cox	2816

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 21 April 2003.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-5,7,8 and 10-19 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) 14-19 is/are allowed.
- 6) Claim(s) 1,3,12 and 13 is/are rejected.
- 7) Claim(s) 2,4-5, 7-8 and 10-11 is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 13 August 2001 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Applicant's arguments with respect to claims 1, 3, and 13 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1, 12, and 13 are rejected under 35 U.S.C. 102(e) as being anticipated by Friedrich et al. (U.S. Patent No. 6,433,599).

In reference to claim 1, Friedrich discloses in Figure 1 an apparatus comprising a first circuit (1, 2, 3, 4, 5) configured to generate a second reference signal (CLOCK) in response to (i) a first reference signal (f_{Ref}) and (ii) a timing signal (the output of VCO 4), wherein (a) a frequency and a phase of the second reference signal (CLOCK) are adjusted in response to the first reference signal (f_{Ref}) and (ii) held when the first reference signal (f_{Ref}) is lost and (b) the first reference signal (f_{Ref}) comprises an external timing signal (the signal (f_{Ref}) itself is seen to be an external timing signal), wherein the first circuit comprises a digitally controlled reference loop circuit; and a second circuit (6, 7, 8) configured to generated one or more output signals (output of VCO 7) in response

to the second reference signal (CLOCK) and one of the one or more output signals (output of VCO 7), wherein the one or more output signals has a controlled and/or substantially zero delay with respect to the first reference signal (f_{Ref}). The same applies to claims 12 and 13 (wherein the apparatus is implemented on a single integrated circuit chip (see ABSTRACT)).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Friedrich et al. (U.S. Patent No. 6,433,599).

In reference to claim 3, Friedrich discloses all the limitations of the claim with respect to claim 1 as mentioned above including the phase locked loop (6, 7,8) configured to generate a clock signal in response to the second reference signal (CLOCK) and the one or more output signals (output of VCO 7). Friedrich does not disclose that the second circuit comprises a buffer circuit configured to generate the one or more output signals in response to the clock signal. However, it would have been obvious to one skilled in the art at the time of the invention that a buffer could be added at the output of the VCO (7) for the advantage of cleaning up the output signal of the

Art Unit: 2816

VCO (7) so that the phase locked loop can more accurately synchronize the output signal with the second reference signal (CLOCK).

Allowable Subject Matter

5. Claims 14-19 are allowed.
6. Claims 2, 4-8, and 10-11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
7. The following is a statement of reasons for the indication of allowable subject matter: Claims 2, 7-8 are allowable because the closest prior art of record fails to disclose a circuit as shown in Figure 2 wherein the first circuit (110) comprises an oscillator 122 configured to generate the second reference signal (REF) in response to the control signal (Ctrl) and the timing signal (Xin) in combination with the rest of the limitations of the base claim and any intervening claims. Claim 4 would be allowable because the closest prior art of record fails to disclose a circuit as shown in Figure 3 wherein the second circuit (112) further includes a divide-by-N circuit (170) in combination with the rest of the limitations of the base claims and any intervening claims. Claims 5 and 10 would be allowable because the closest prior art of record fails to disclose a circuit as shown in Figure 2 wherein the PLL (140) is an analog circuit in combination with the rest of the limitations of the base claim and any intervening claims. . Claim 11 would be allowable because the closest prior art of record fails to disclose a circuit as shown in Figure 3 wherein the first circuit (110) further comprises a divide-by-

N circuit (160) in combination with the rest of the limitations of the base claims and any intervening claims.

8. The following is an examiner's statement of reasons for allowance: Claims 14-19 are allowed because the closest prior art of record fails to disclose a circuit as shown in Figure 3 wherein the second reference signal (Ref) comprises a crystal oscillator signal (X_{IN}) in combination with the rest of the limitations of the base claims and any intervening claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Cassandra Cox whose telephone number is 703-306-5735. The examiner can normally be reached on Monday-Thursday from 8:00 AM to 5:30 PM and on alternate Fridays from 8:00 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on (703)-308-4876. The fax phone numbers for the organization where this application or proceeding is assigned are 703-872-9318 for regular communications and 703-872-9319 for After Final communications.

Application/Control Number: 09/928,818
Art Unit: 2816

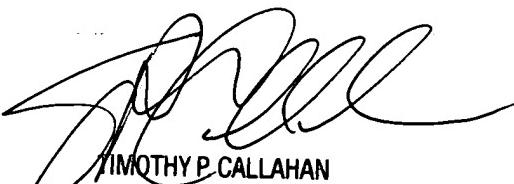
Page 6

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

CC

CC

June 27, 2003



TIMOTHY P. CALLAHAN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800